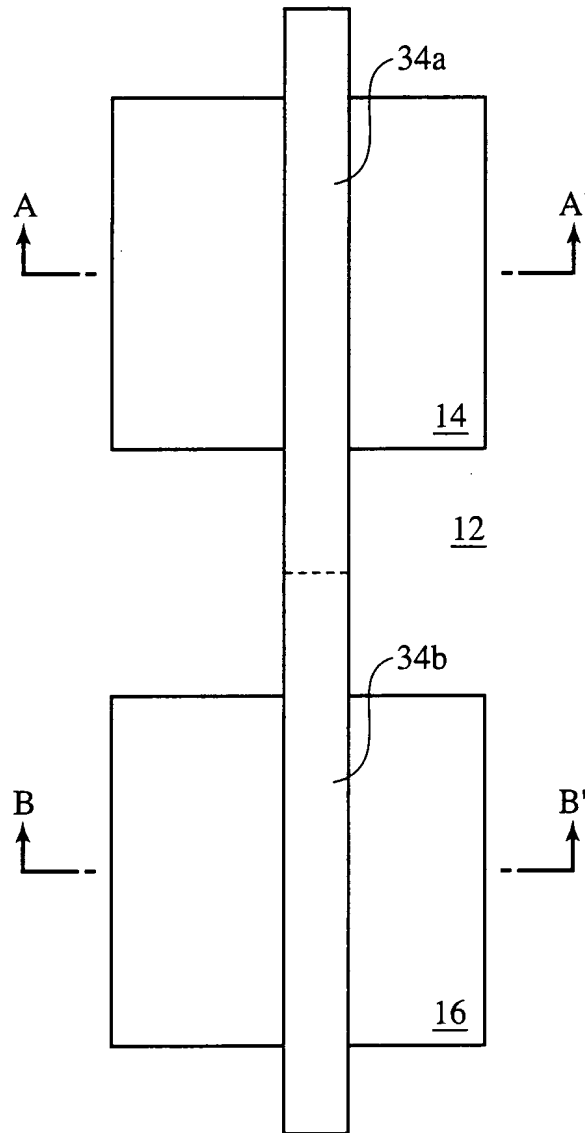


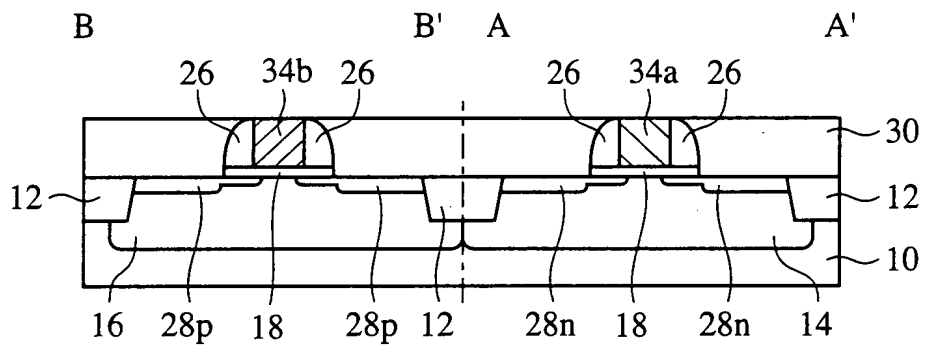
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FIG. 1



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FIG. 2





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FIG. 4A

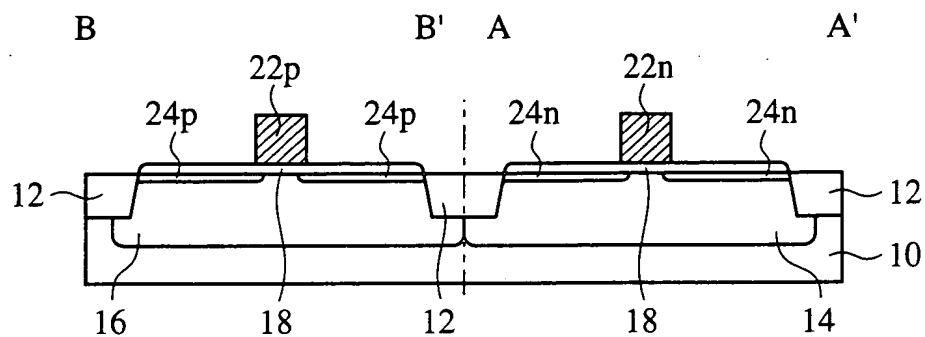


FIG. 4B

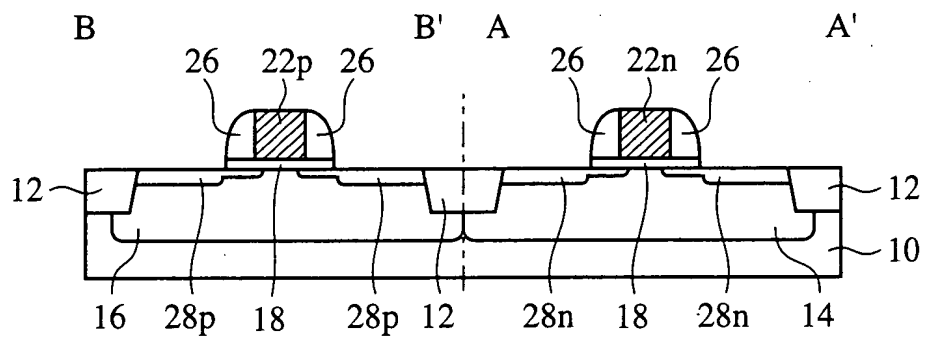
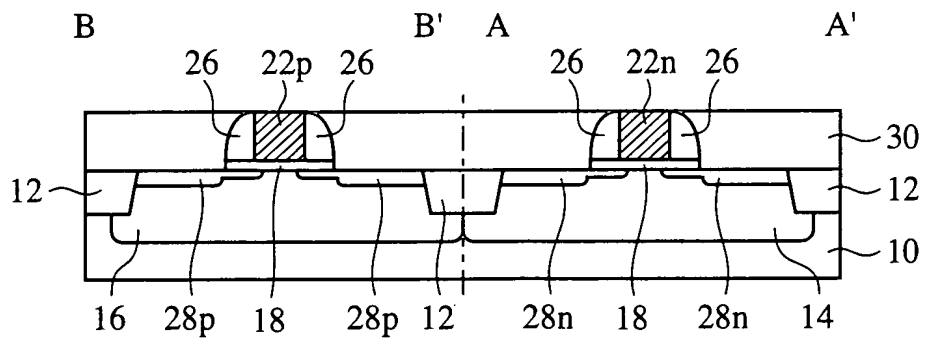


FIG. 4C



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FIG. 5A

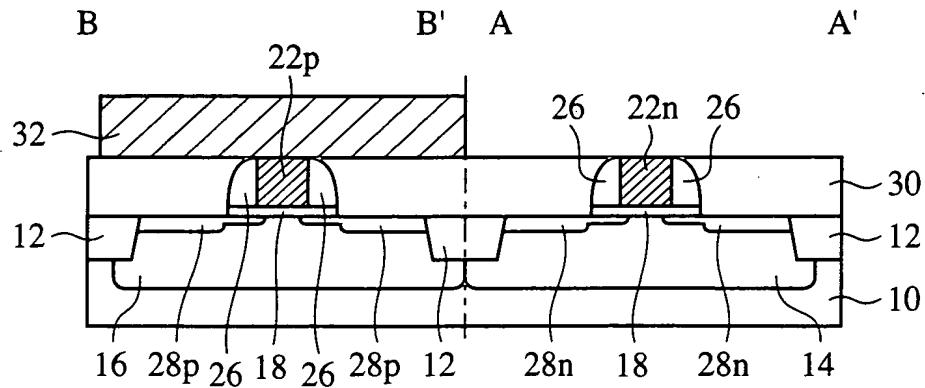


FIG. 5B

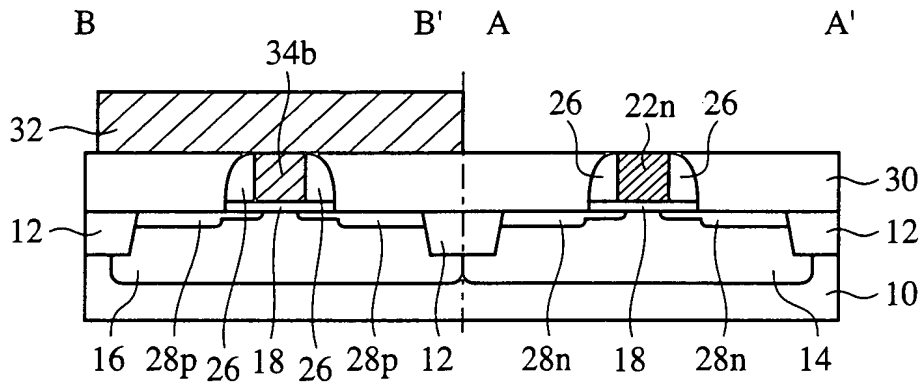
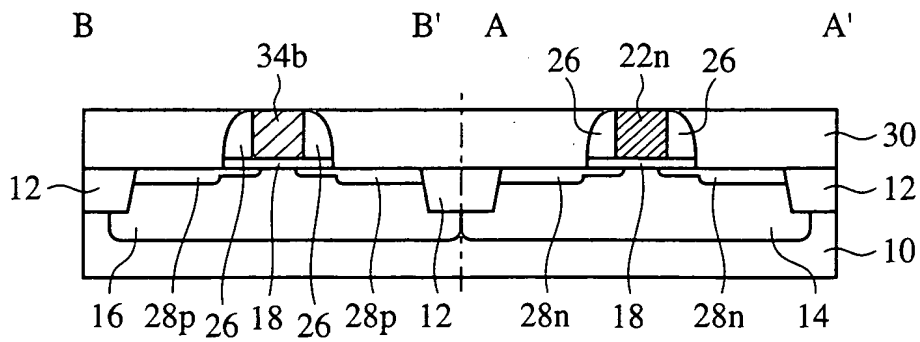


FIG. 5C



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FIG. 6A

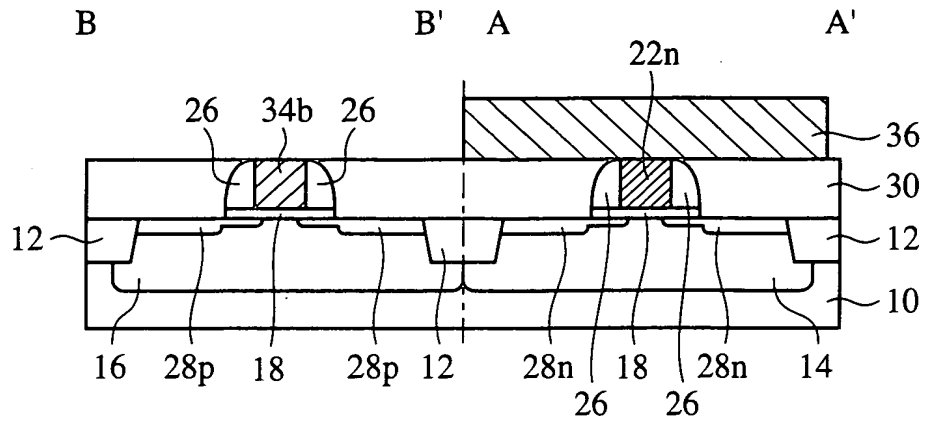


FIG. 6B

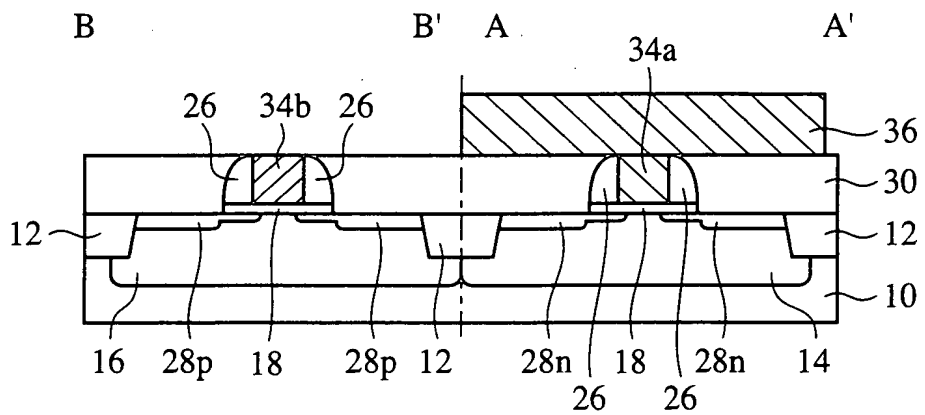
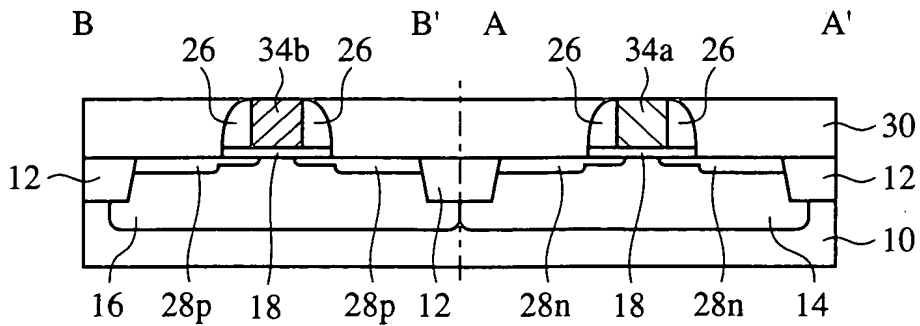
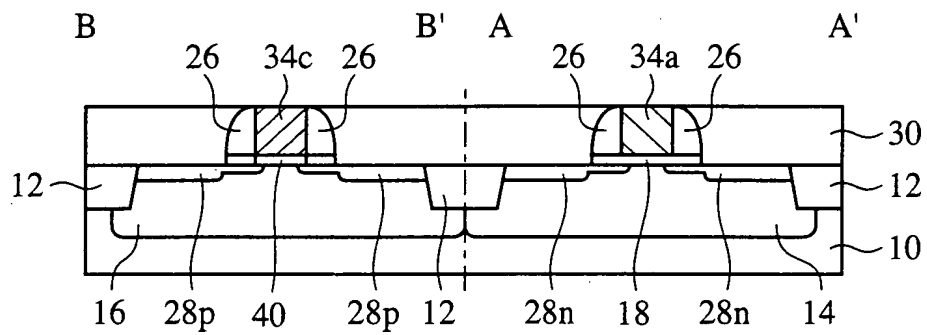


FIG. 6C



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FIG. 7





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FIG. 9A

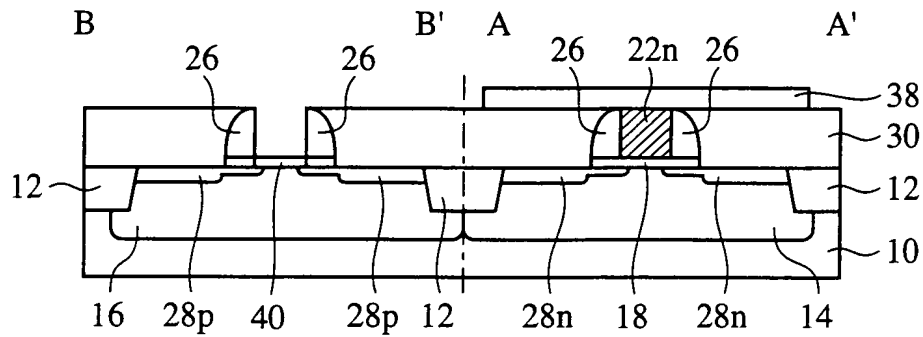


FIG. 9B

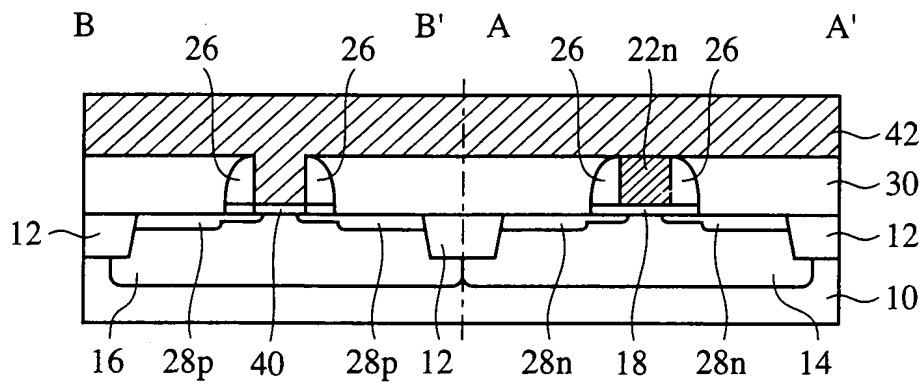
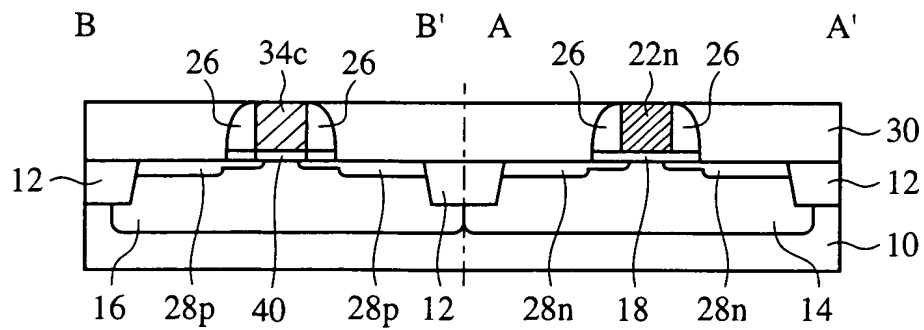


FIG. 9C



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FIG. 10A

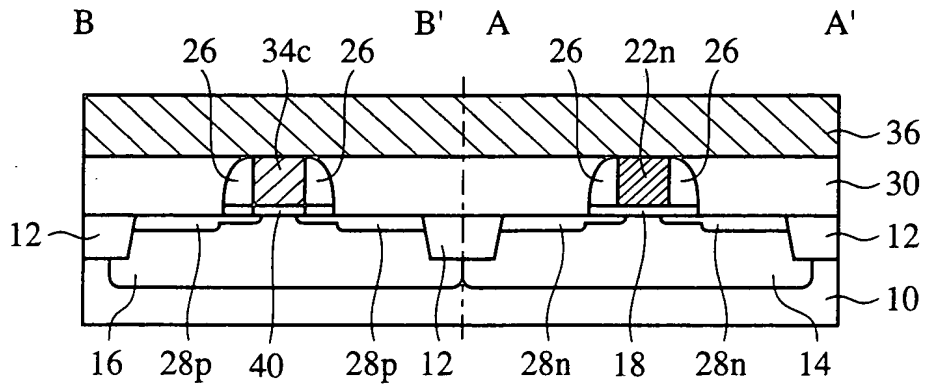


FIG. 10B

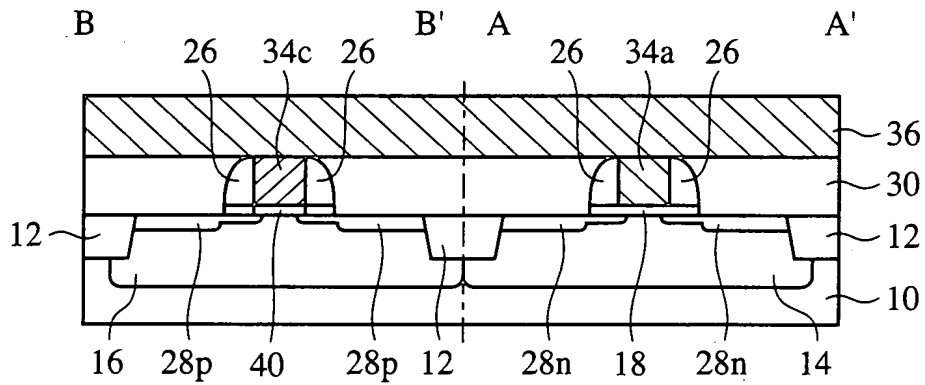
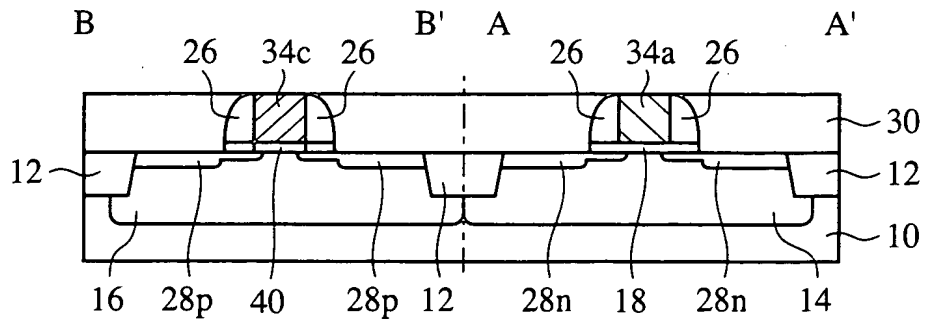
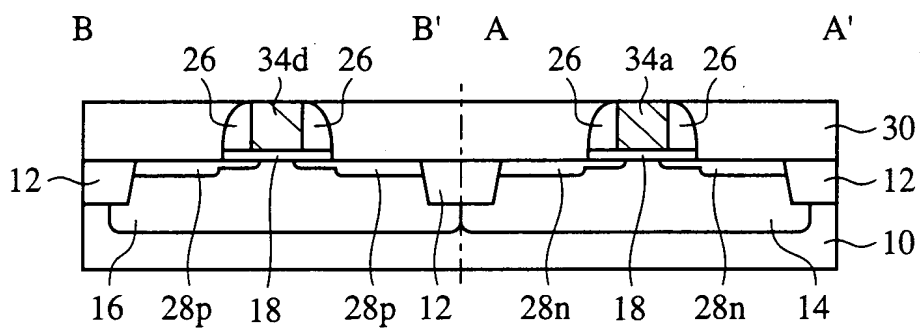


FIG. 10C



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FIG. 11



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FIG. 12A

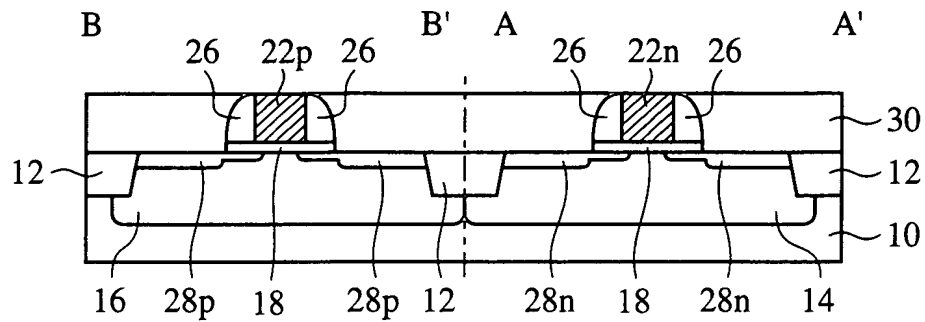


FIG. 12B

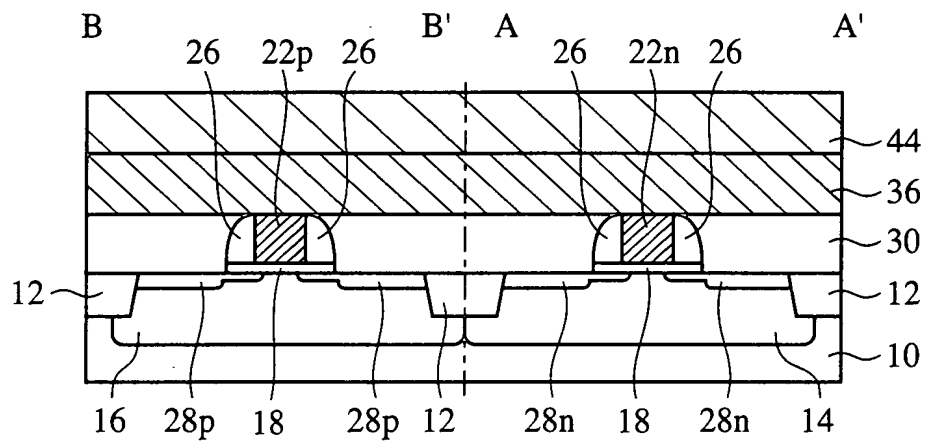
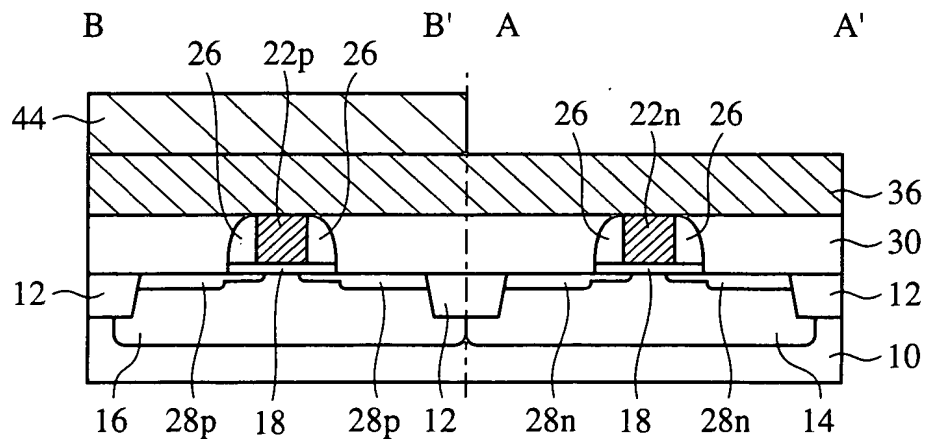


FIG. 12C



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FIG. 13A

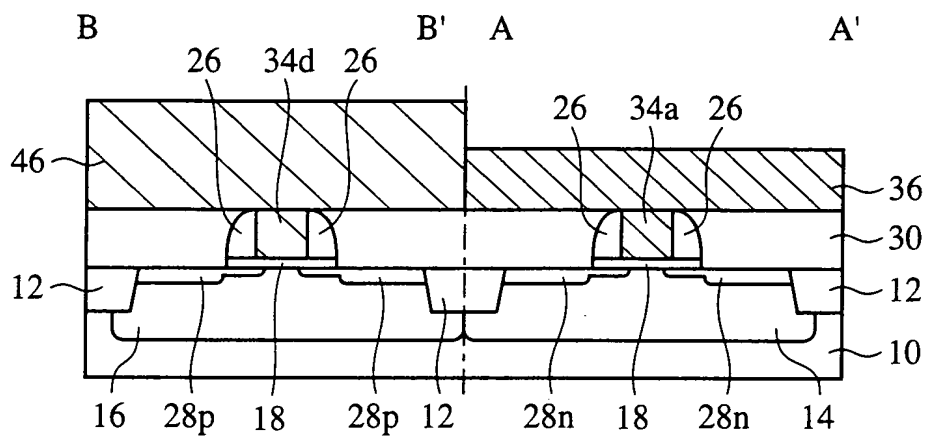
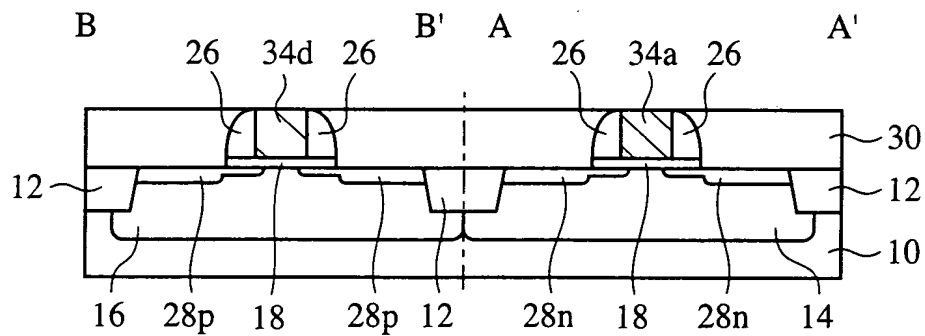
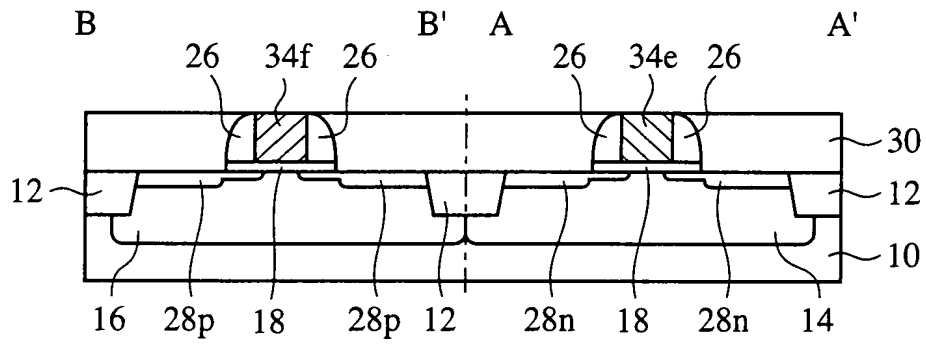


FIG. 13B



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FIG. 14



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FIG. 15A

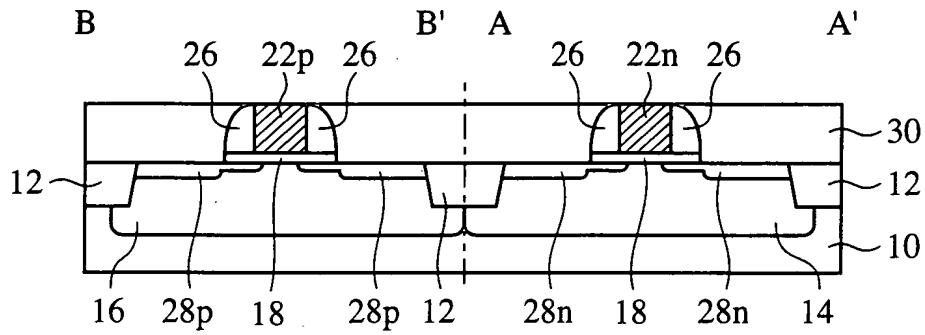


FIG. 15B

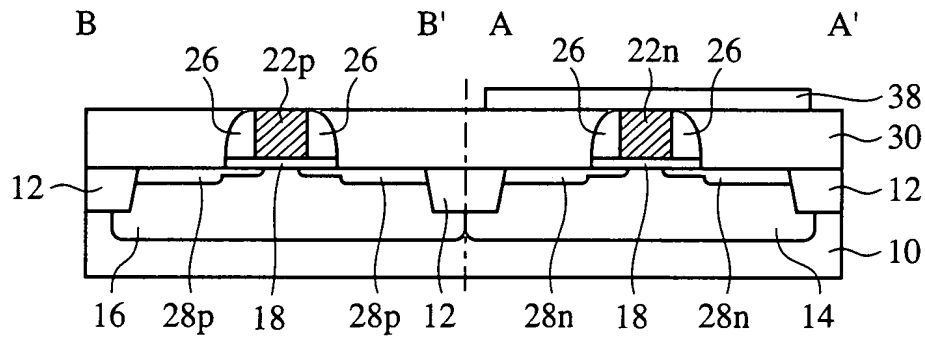
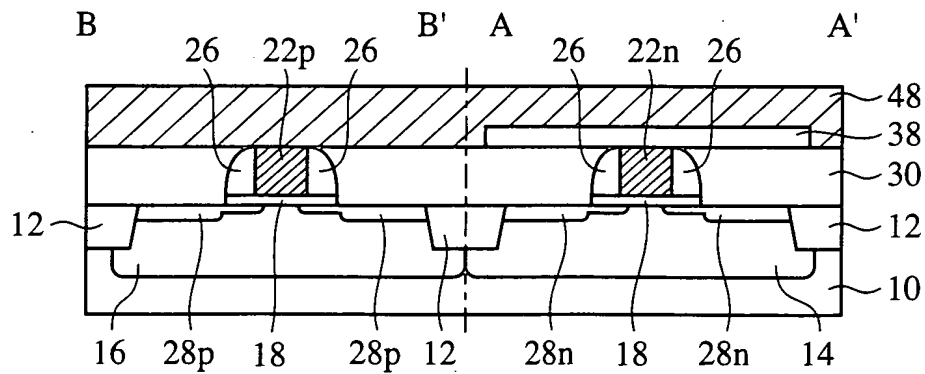
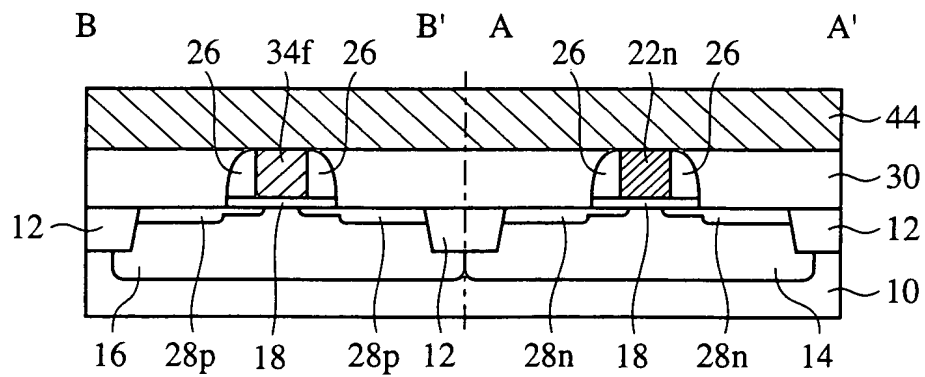


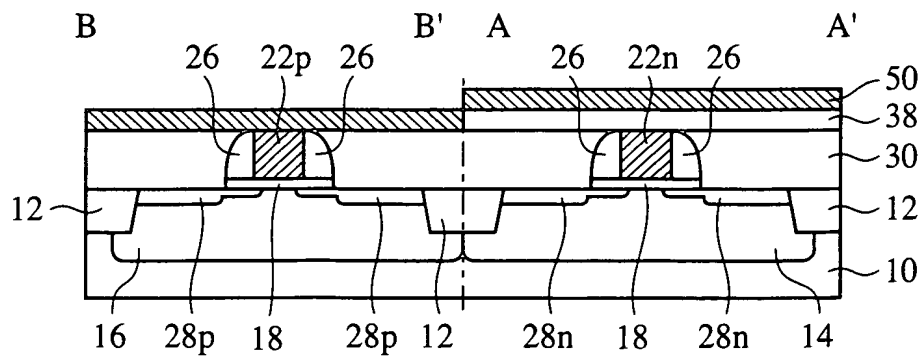
FIG. 15C

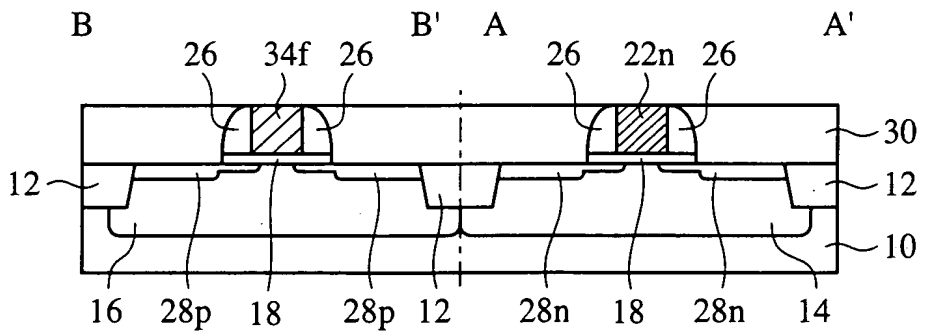








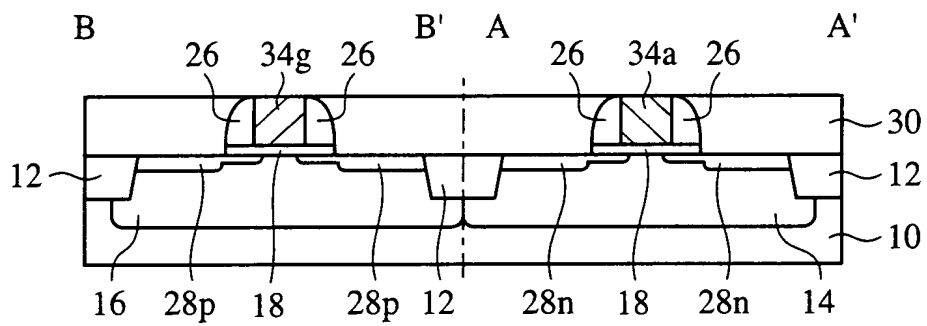




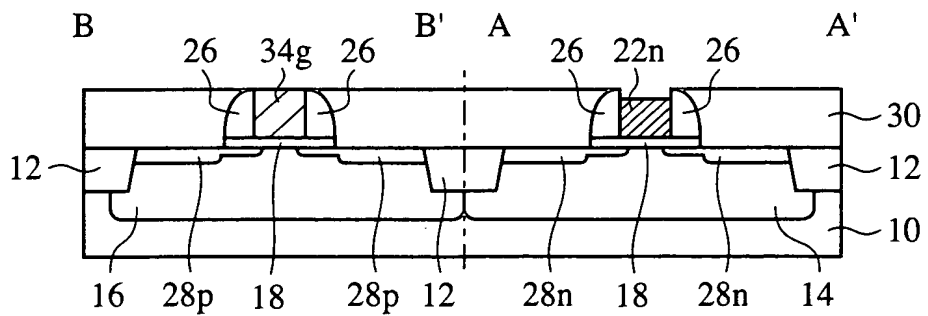


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FIG. 22



This cross-sectional view shows a semiconductor device with two types of semiconductor regions. The device consists of a substrate 10 with a top layer 30. A central region 12 is defined by a dashed line. On either side of the center, there are two types of semiconductor regions: 22p (p-type) and 22n (n-type). These regions are surrounded by a layer 26. The regions 22p and 22n are separated by a layer 18. The regions 22p and 22n are also separated by a layer 16. The regions 22p and 22n are also separated by a layer 28p and 28n. The regions 22p and 22n are also separated by a layer 14.



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FIG. 25A

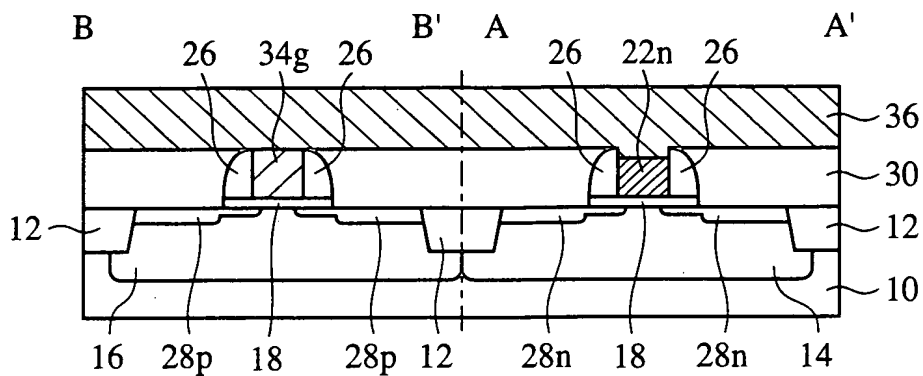


FIG. 25B

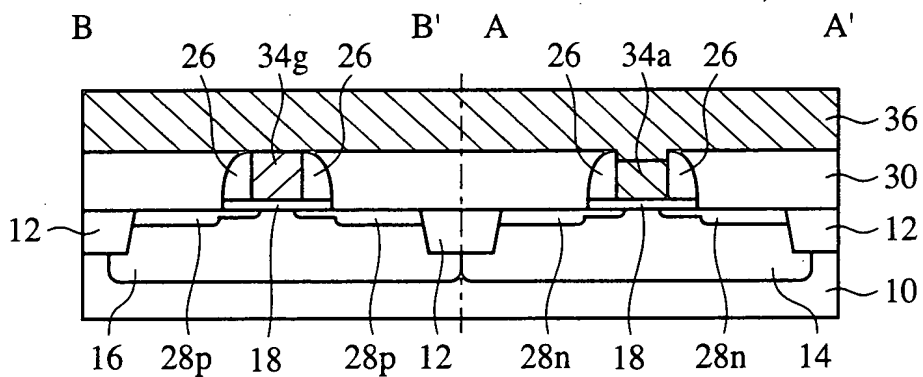
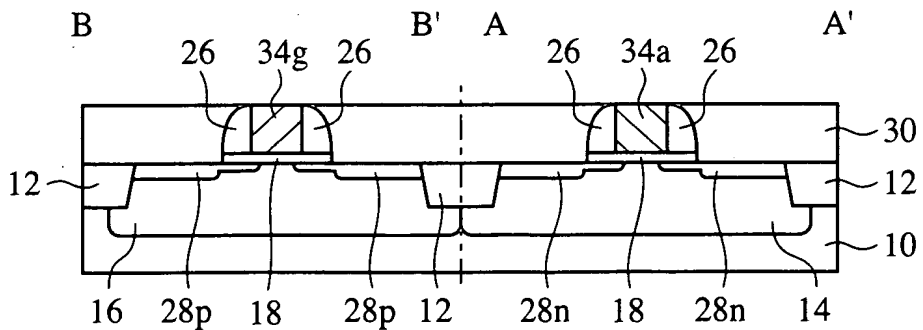
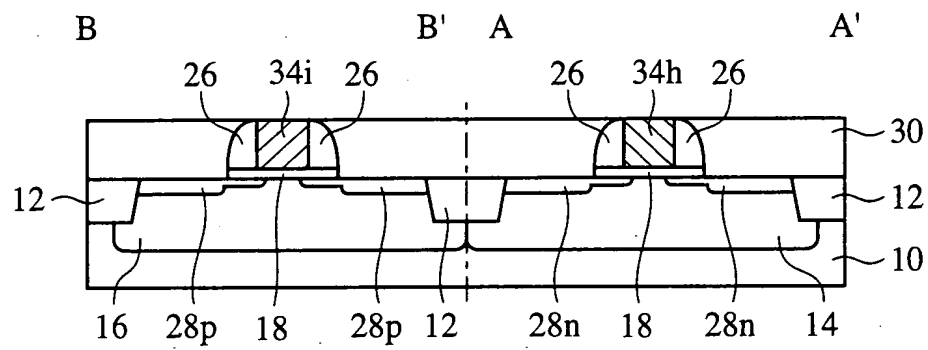


FIG. 25C



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FIG. 26



[illegible]